**Flow the following instructions for VERILOG Programming**

1. Open <http://bleyer.org/icarus/>
2. Download : iverilog-0.9.6\_setup.exe
3. After downloading go to desktop open Icarus Verilog
4. Go to bin folder
5. Open a new text document
6. Write the verilog code
7. Go to save as
8. Give the file extension name .vl ( selecting all files from save as type)
9. .vl file will be created in bin folder
10. In the address bar type cmd
11. Command for compilation is :

iverilog –o < exe file name> example.vl example\_test.vl

Where example.vl is the verilog code

And example\_test.vl is the test bench file name

1. Command for Run: vvp <exe file name>
2. Command for running gtkwave file : gtkwave < vcd file name>